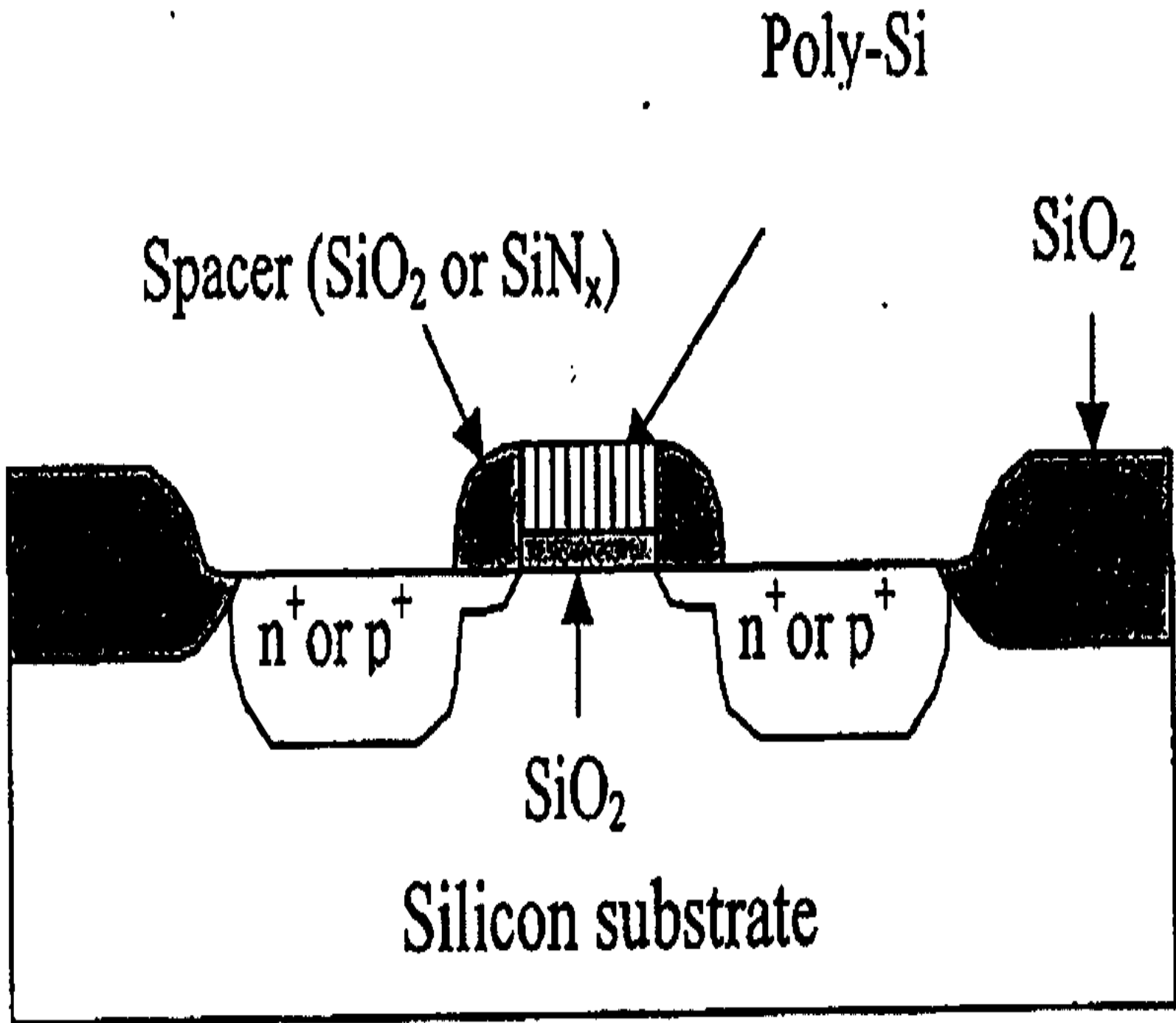


CMOS structure

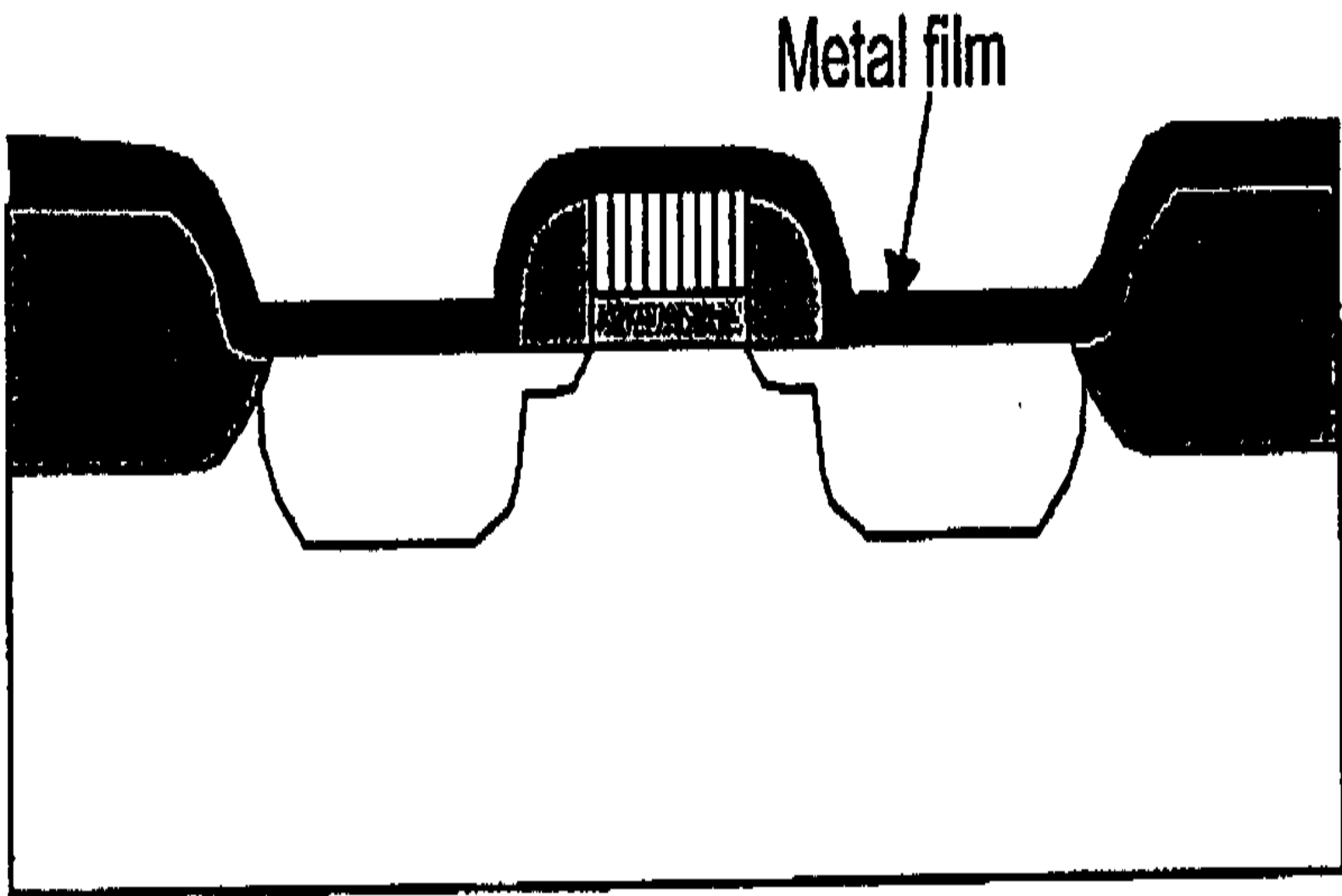
Figure 1

PRIOR ART



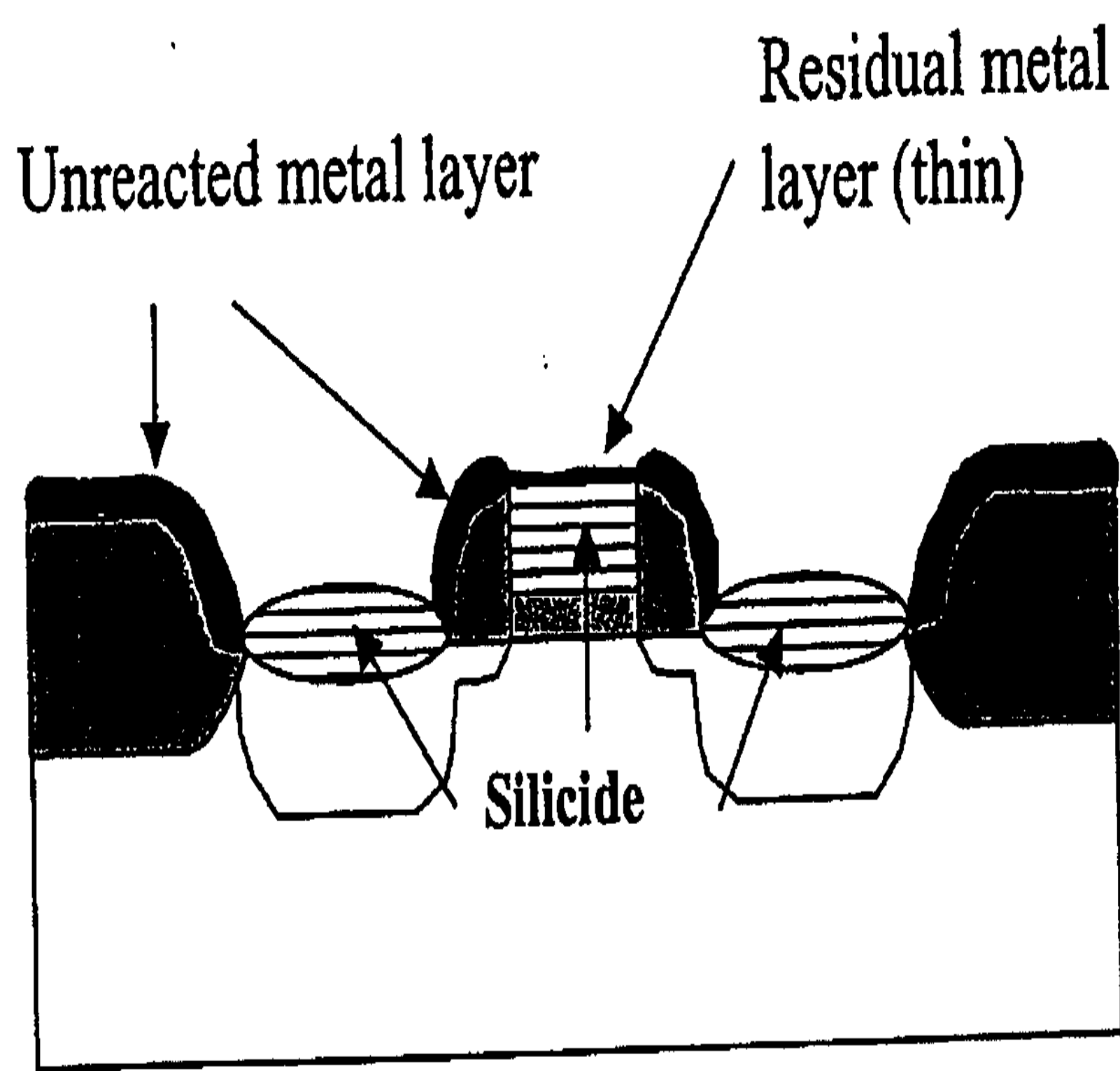
(1) Before metal deposition

Figure 2 (1)



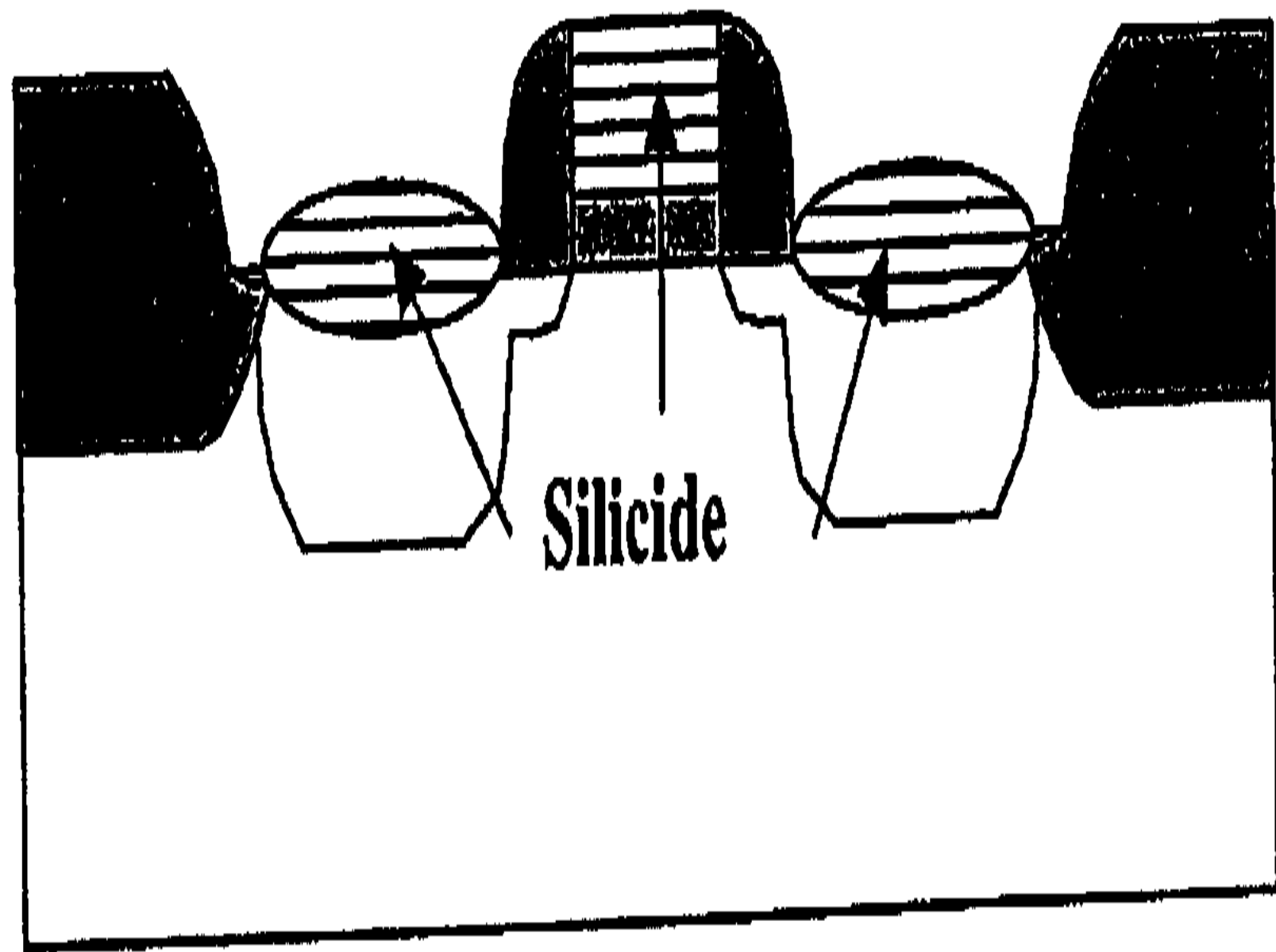
(2) After metal deposition

Figure 2(2)



(3) After silicide formation

Figure 2 (3)



(4) After metal etch-off

Figure 2 (4)

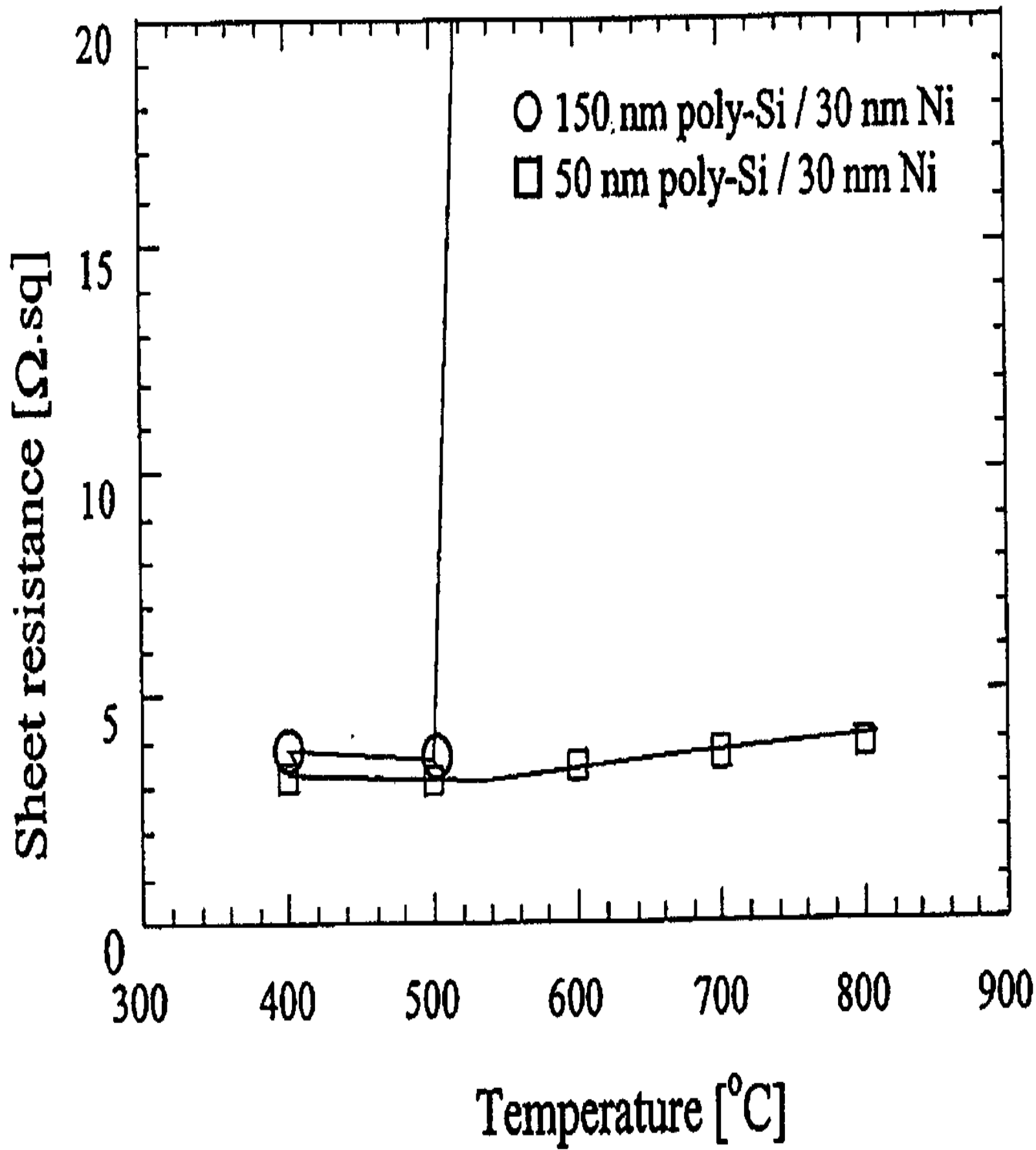


Figure 3

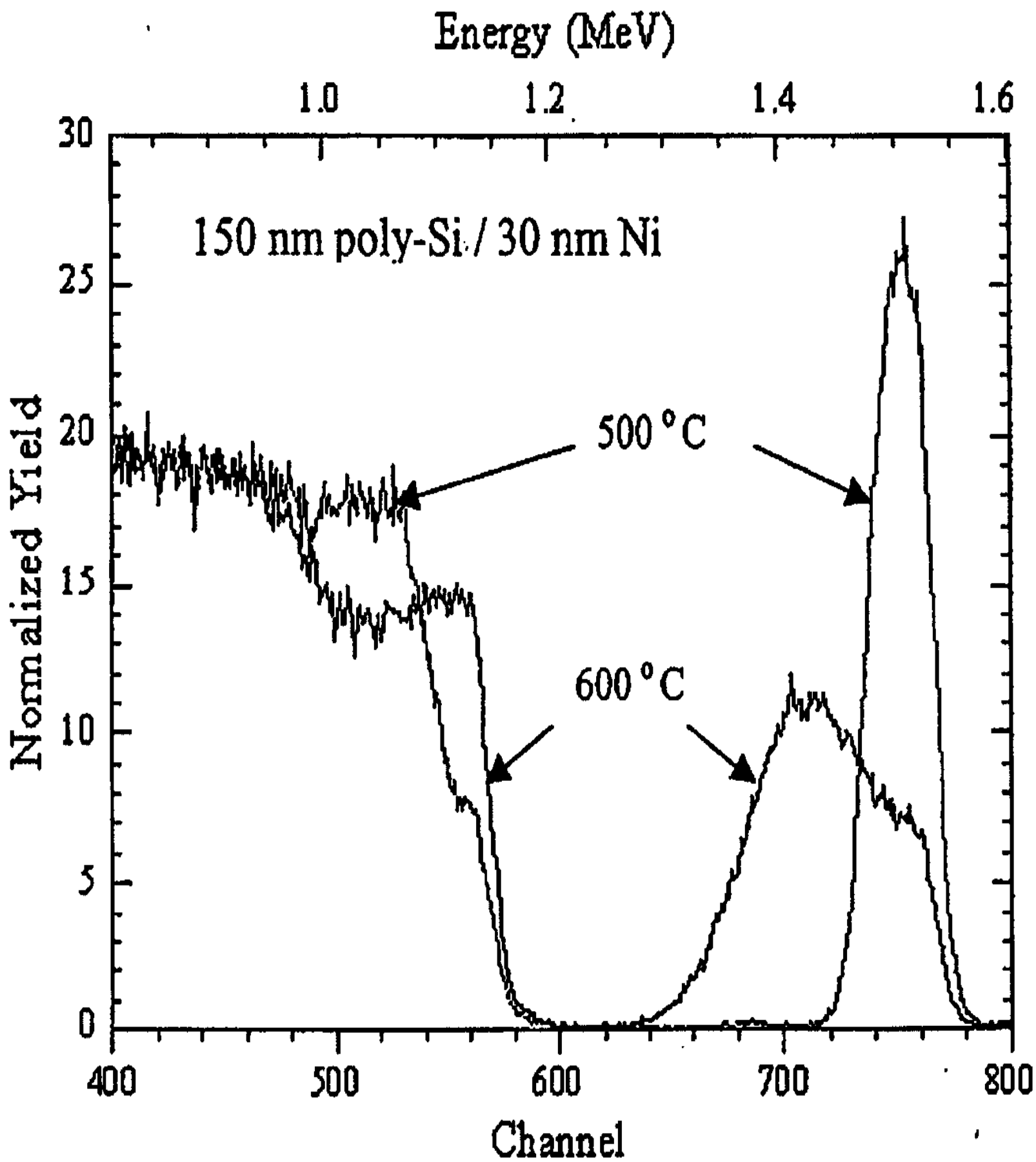


Figure 4